



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Oelcer et al.

Docket No.: CH920000087US1

Serial No.: 10/045,810

Group Art Unit:

Filed: January 11, 2002

Examiner:

For: BLOCK CODING FOR MULTILEVEL DATA COMMUNICATION

Commissioner for Patents  
Washington, D.C. 20231

**CERTIFICATE OF MAILING UNDER 37 CFR 1.8(A)**

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Information Disclosure Statement  
PTO 1449 Form with references

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on February 8, 2002

Margaret McCabe

Margaret McCabe  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

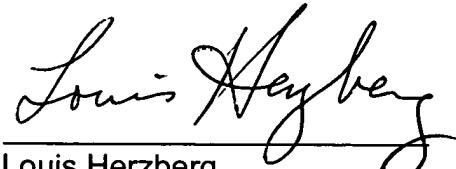
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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D. C. 20231

Sir:

In order to assist in the examination of this application, and in accordance with 37 CFR 1.56, 1.97 and 1.98, copies of the documents listed on the attached form PTO-1449 are forwarded herewith.

By:   
Louis Herzberg  
Reg. No. 41,500  
(914) 945-2885

IBM Corporation  
Intellectual Property Law Dept.  
P. O. Box 218  
Yorktown Heights, New York 10598

<b>FORM PTO-1449 (Modified)</b> <b>LIST OF PATENTS AND PUBLICATIONS</b> <b>FOR APPLICANT'S INFORMATION</b> <b>DISCLOSURE STATEMENT</b>	ATTY. DOCKET NO. CH920000087US1	SERIAL NO. 10/045,810
	APPLICANT: Oelcer et al.	
	(Use several sheets if necessary)	
	FILING DATE: January 11, 2002	GROUP:

REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS					
EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)	
AA							
AB							
AC							
AD							
AE							
AF							
AG							
AH							
AJ							
AJ							
AK							

#### FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
AL							

#### OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

AM	"Reduced Complexity Iterative Decoding of Low-Density Parity Check Codes Based on Belief Propagation", Marc P.C. Fossorier, Miodrag Mihaljevic, Hideki Imai.
AN	"Irregular Repeat-Accumulate Codes": Hui Jin, Aamod Khandekar, and Robert McEliece; Dept. of Electrical Eng., California Institute of Technology
AO	"Reed-Muller Coding for Partial Response Channels", Sedat Oelcer and Gottfried Ungerboeck; IBM Research Div., Zurich Research Lab. Switzerland
AP	"Bandwidth Efficient Low Density Parity Check Coding using Multi Level Coding and Iterative Multi Stage Decoding", K.R. Narayanan and J. Li; Texas A&M University, College Sta. Texas.
AQ	Catena Networks, Inc., "A Turbo TCM scheme with Low Decoding Complexity" Bangalore, India, Study group.
AR	Mitsubishi Elec. Corp.: "Proposal of Decision making process for Turbo Coding & report of performance evaluation of proposed TTCM(PCC) with R-S code and without R-S code.
AS	EVOCAL Technologies Ltd., "Results of requirements requested in the Coding Ad hoc report(BA-108R1) for the proposed Turbo Codes for ADSL modems by VoCAL Technologies Ltd. in BA-020R1.
AT	Globespan: "Block Product Turbo Codes for G.dmt.bis and G.lite.bis.
AU	Aware, Inc. "Low Density Parity Check Coded Modulation for ADSL.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.